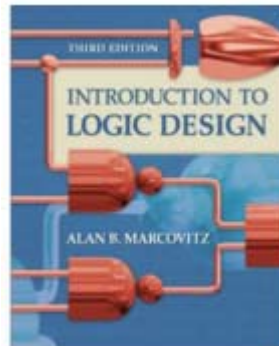# Syllabus – Digital Logic

Course Information (1/3)

- Course: Digital Logic

- Instructor: David Shiung (熊大爲)

- Emal: davids@cc.ncue.edu.tw

- Time & Venue: Thur. 6-8, E404

- Credit: 3 Hours; 3 Credits

- Textbook: Alan B. Marcovitz, Introduction to Logic Design, 3rd Edition, McGraw Hill, 2010.

Course Information (2/3)

- Website:
  http://eedept.ncue.edu.tw/teacher/file_list.php?course_ID=277

- Course pre-requisites: Non

- Scoring: Midterm 35%, Final exam 35%, Homework 30%

Course Information (3/3)

- Course outline:
    - Chapter 1   Introduction
    - Chapter 2   Combinational Systems
    - Chapter 3   The Karnaugh Map
    - Chapter 4   Function Minimization Algorithms
    - Chapter 5   Designing Combinational Systems
    - Chapter 6   Analysis of Sequential Systems
    - Chapter 7   The Design of Sequential Systems
    - Chapter 8   Solving Larger Sequential Problems
    - Chapter 9   Simplification of Sequential Circuits